## ABSTRACT OF THE DISCLOSURE

A method for representing root busses and their subordinate bus configurations using an object oriented abstraction scheme that enables various system components to communication with peripheral devices attached to the root busses and their subordinate busses without requiring specific knowledge of the access mechanisms of those devices. During the initialization process of a platform, a core dispatcher loads a PCI bus plug-in (PPI) for each entity that can create a root bus. When the plug-in for an entity is loaded, it produces a GUIDed object called a GRB (GUID of PPI for Root Bus) that provides an abstracted representation of the root buss's configuration and resources. The GRB includes a plurality of components including driver methods that are registered with the core authority, which then publishes the GRB to enable access to the methods and resources. An enumeration process is performed on each root bus in a system to determine its configuration and resources through use of the methods.